

Electronic Filing System (EFS) Data

Electronic Patent Application Submission

USPTO Use Only

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Application ID: 10710692 
Title of Invention: High Voltage Circuits
Implemented Using Low Voltage
Transistors
First Named Inventor: Sanish JACOB
Domestic/Foreign Application: Domestic Application
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FEE RECORD SHEET

07/29/2004 HDEMESS1 00000037 200668 10710692

01 FC:1001 770.00 DA
02 FC:1201 774.00 DA
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